

AMENDMENTS TO THE SPECIFICATION:

Please replace the paragraph beginning at page 6, line 13, with the following amended paragraph:

The differential amplifier 20 in the embodiment shown includes a pair of individual inductor components L3 and L4 that may be used for transistor biasing and output port matching in the differential amplifier 20. The inductor components L3 and L4 may be omitted from the circuit. Alternatively as illustrated in FIG. 4, the inductor components L3 and L4 may be replaced with a transformer T4, ~~such as~~ like the transformer T1 in FIG. 1, to yield further circuit space savings and improved common mode rejection.

Please replace the paragraph beginning at page 7, line 3, with the following amended paragraph:

The differential amplifier 30 in the embodiment shown includes a pair of individual inductor components L5 and L6 that may be used for transistor biasing and output port matching for the differential amplifier 30 or that may be omitted from the circuit. Alternatively as illustrated in FIG. 5, the inductor components L5 and L6 may be replaced with a transformer T5, ~~such as~~ like the transformer T1 in FIG. 1, to yield circuit space savings and additional common mode rejection.

Please replace the paragraph beginning at page 6, line 24, with the following amended paragraph to correct a typographical error:

FIG. 3 shows a differential amplifier 30 according to the present teachings that includes a pair of transistors Q5-Q6 and a pair of mutually coupled inductors in the form of a transformer T3 for providing input impedance matching and for noise. The transformer T3 is wired so that the effective inductances of its individual inductor windings are augmented by the mutual coupling when the differential amplifier 30 ~~20~~ is excited in the common mode. Thus, the transformer T3 improves the common mode rejection of the differential amplifier 30.